

AMENDMENTS TO THE CLAIMS

1. (Original) A method of terminating an affected application program thread, comprising:
receiving an indication of a hardware error associated with an application program thread;
determining the application program thread to be in a user operation mode; and
terminating the application program.
2. (Original) The method of claim 1, wherein the terminating the application program further comprises:
determining the hardware error is a memory read error, the memory read error being associated with the application program thread.
3. (Original) The method of claim 2, further comprising:
determining the memory read error is successfully contained.
4. (Original) The method of claim 3, further comprising:
receiving information of whether the memory read error is contained.
5. (Original) The method of claim 2, further comprising:
receiving information of whether the hardware error occurred on a memory read.
6. (Original) The method of claim 1, further comprising:
receiving information of a poisoned data address associated with the hardware error.
7. (Original) The method of claim 1, further comprising:
confirming one or more registers associated with the application program thread are consumed.
8. (Currently Amended) A system comprising:
a processor to perform an instruction from an operating system; and

a memory component to provide machine error information to the operating system, the machine error information to include an operation mode of the ~~an~~-affected application program, the operating system to terminate the affected application program thread upon determining the affected application program to be within a user operation mode.

9. (Original) The system of claim 8, wherein the processor is to receive an instruction from the operating system to terminate the affected application program thread upon determining a memory read error has occurred.

10. (Original) The system of claim 9, wherein the processor is to receive an instruction from the operating system to terminate the affected application program thread upon determining the memory read error is contained.

11. (Original) The system of claim 9, wherein the operating system is to check to the machine error information message to determine whether the memory read error occurred.

12. (Original) The system of claim 11, wherein the operating system is to check the machine error information message to determine whether the memory read error is contained.

13. (Original) A machine-accessible medium that provides instructions that, if executed by a machine, will cause the machine to perform operations comprising:

receiving an indication of a hardware error associated with an application program thread;
determining the application program thread to be in a user operation mode; and
terminating the application program.

14. (Original) The machine-accessible medium of claim 13, wherein the terminating the application program further comprises:

determining the hardware error is a memory read error, the memory read error being associated with the application program thread.

15. (Original) The machine-accessible medium of claim 14, further comprising:
determining the memory read error is successfully contained.
16. (Original) The machine-accessible medium of claim 15, further comprising:
receiving information of whether the memory read error is contained.
17. (Original) The machine-accessible medium of claim 14, further comprising:
receiving information of whether the hardware error occurred on a memory read.
18. (Original) The machine-accessible medium of claim 13, further comprising:
receiving information of a poisoned data address associated with the hardware error.
19. (Original) The machine-accessible medium of claim 13, further comprising:
confirming one or more registers associated with the application program thread are consumed.
20. (Original) A system comprising:
a means for receiving an indication of a hardware error associated with an application program thread;
a means for determining the application program thread to be in a user operation mode;
and
a means for terminating the application program.
21. (Original) The system of claim 20, wherein the means for terminating the application program further comprises:
a means for determining the hardware error is a memory read error, the memory read error being associated with the application program thread.
22. (Original) The system of claim 21, further comprising:
a means for determining the memory read error is successfully contained.

23. (Original) The system of claim 22, further comprising:
a means for receiving information of whether the memory read error is contained.
24. (Original) The system of claim 23, further comprising:
a means for receiving information of whether the hardware error occurred on a memory read.
25. (Original) The system of claim 20, further comprising:
a means for receiving information of a poisoned data address associated with the hardware error.
26. (Original) The system of claim 20, further comprising:
a means for confirming one or more registers associated with the application program thread are consumed.